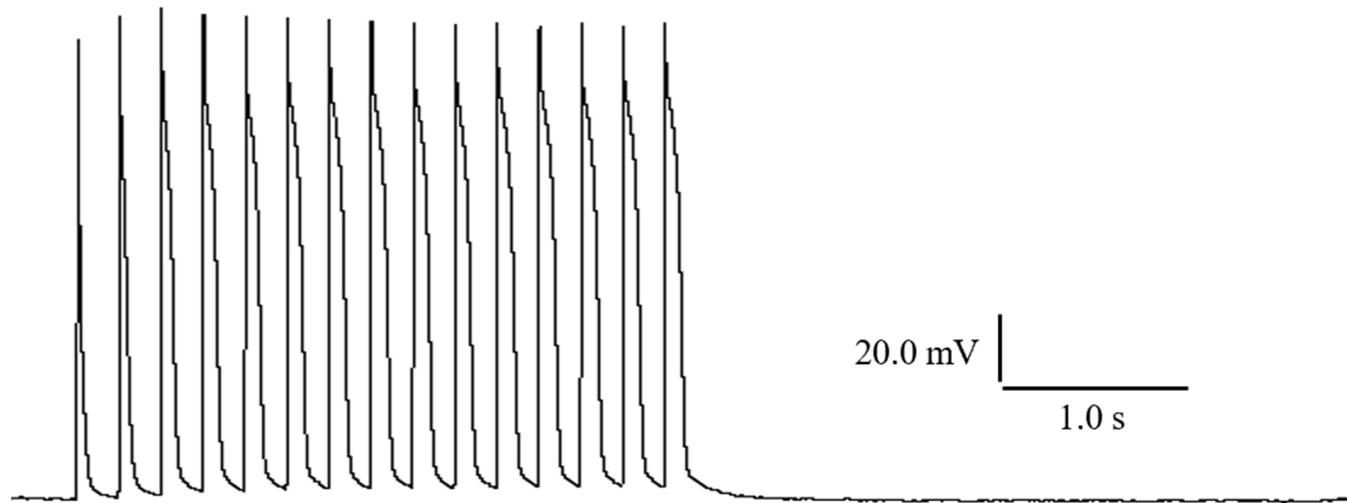
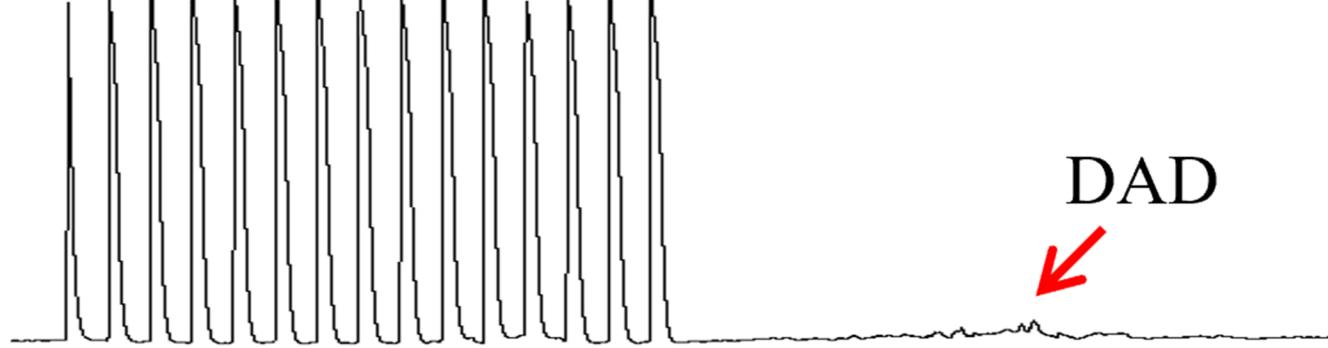
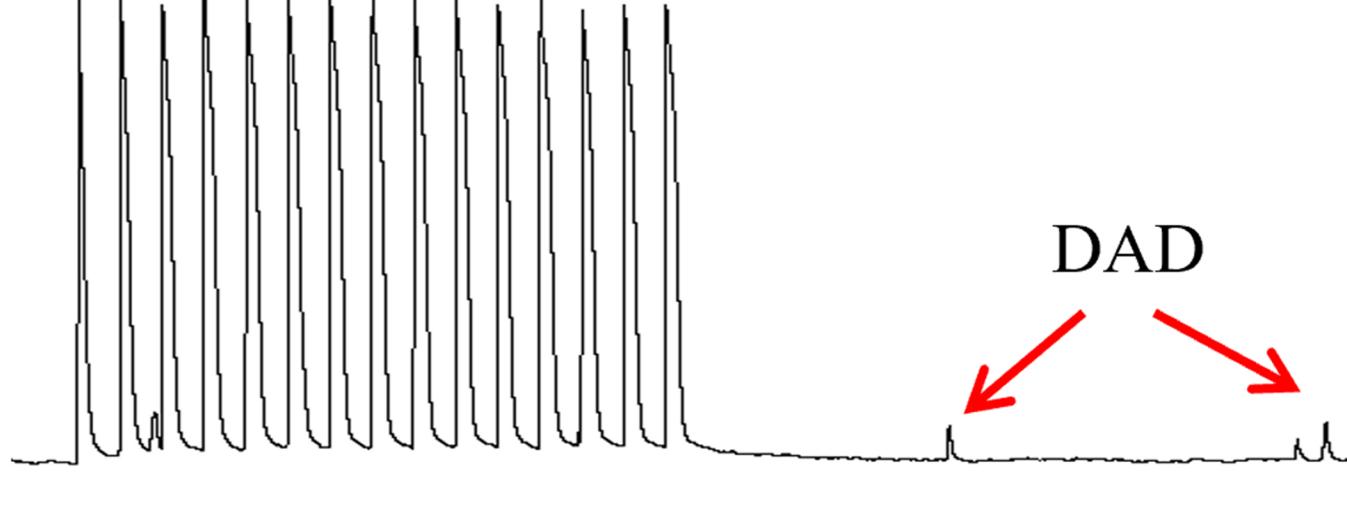
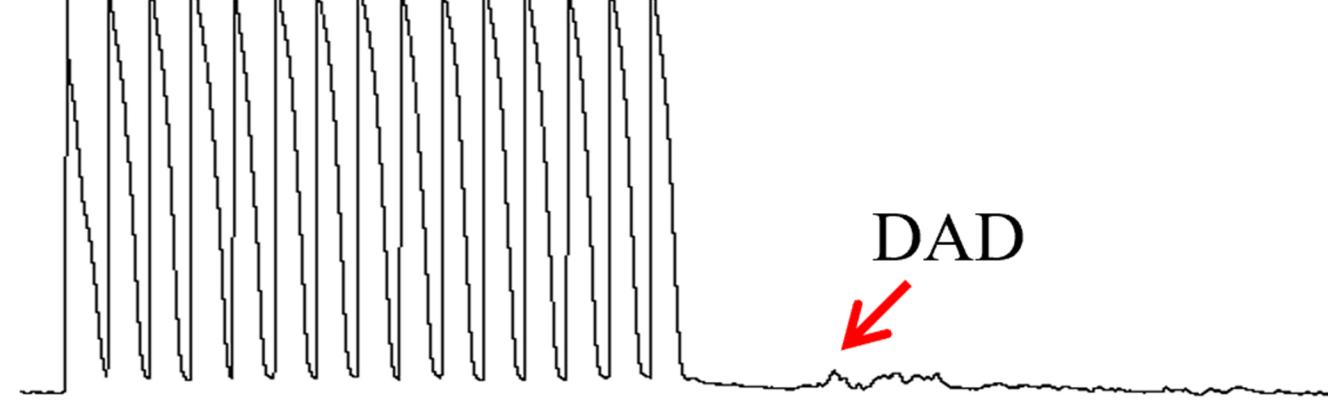


**A**

## a. Control

b. IVA alone (0.3-3  $\mu$ M)c. ACh (0.3  $\mu$ M) + IVA (0.3-3  $\mu$ M)d. ATX II (1 nM) + IVA (0.3-3  $\mu$ M)**B**