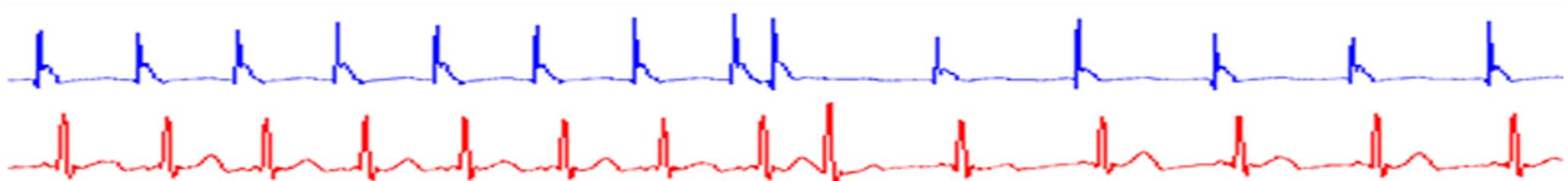
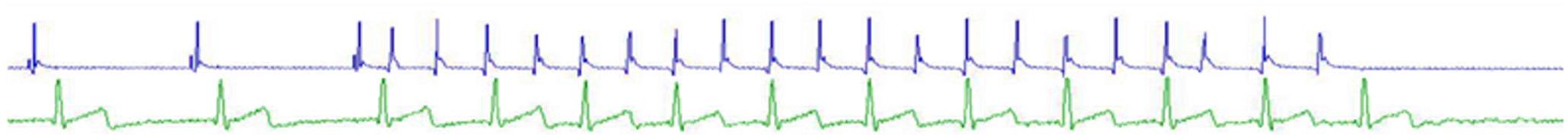


A

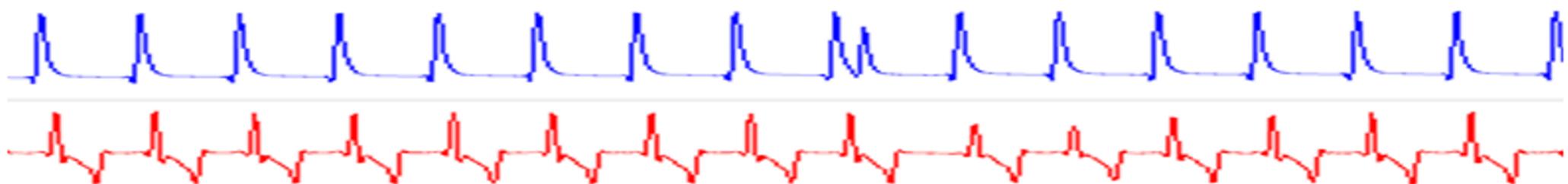
a. Control (CL=350 ms)



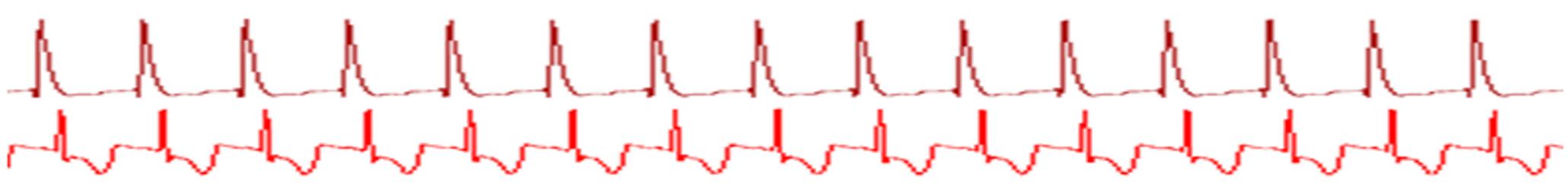
b. Low rate (CL=570 ms)



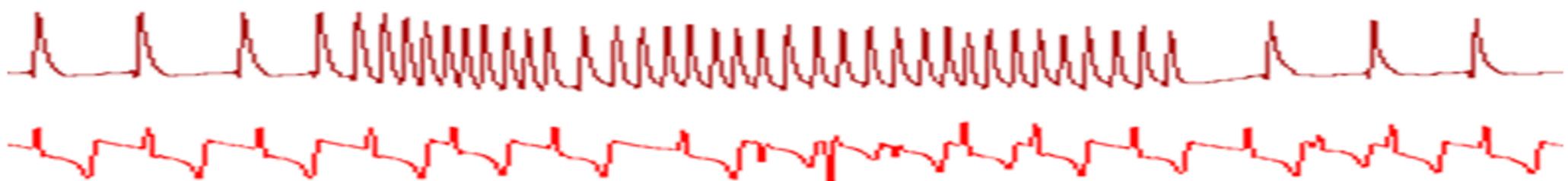
c. IVA (0.3 μM)



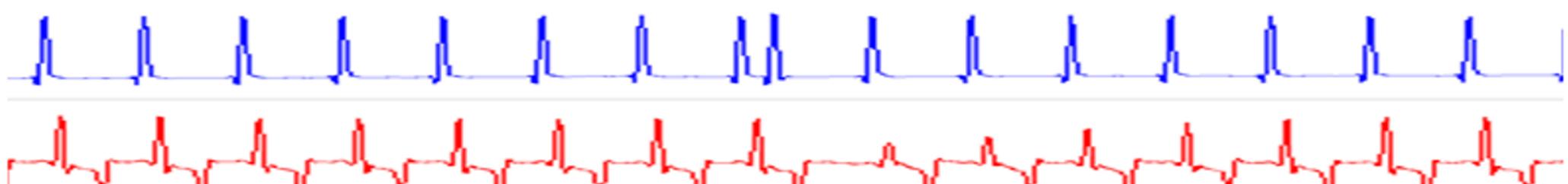
d. ATX-II (2 nM)



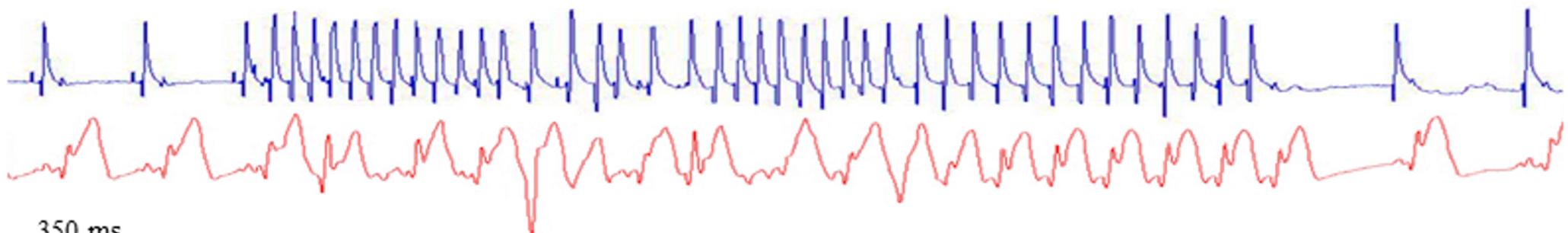
e. ATX-II (2 nM) + IVA (0.3 μM)



f. ACh (0.3 μM)

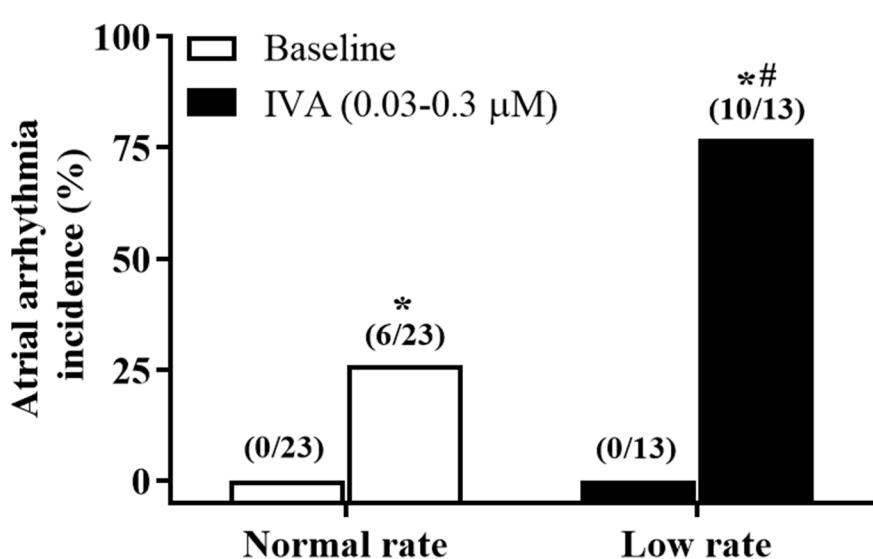


g. ACh (0.3 μM) + IVA (0.3 μM)



350 ms

B



C

